

FIG. 1 Prior art ECC calculator

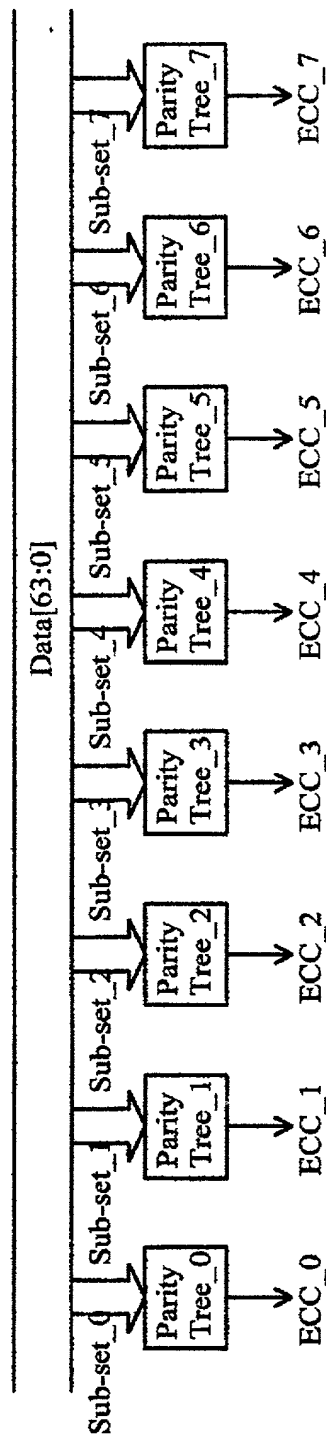
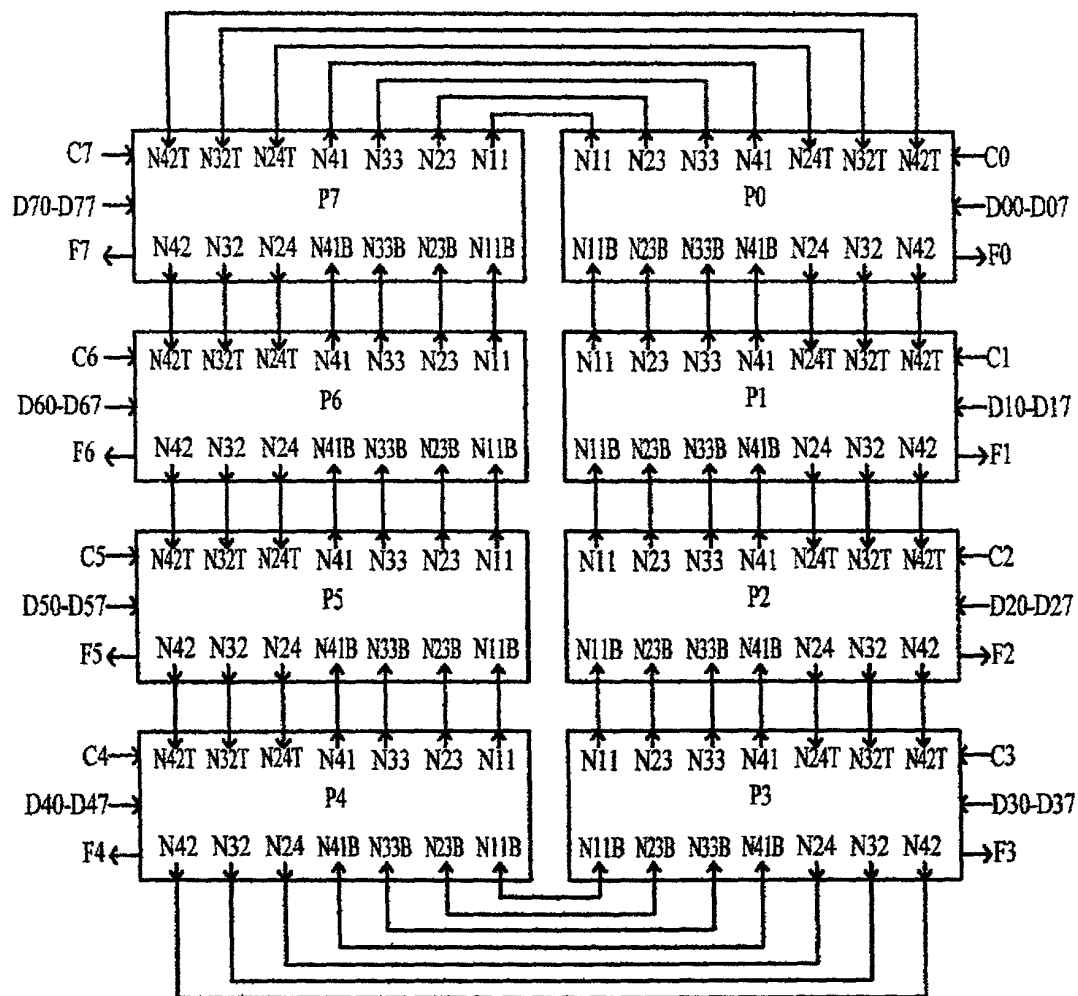


FIG. 2(a)



TOP SECRET

FIG. 2(b)

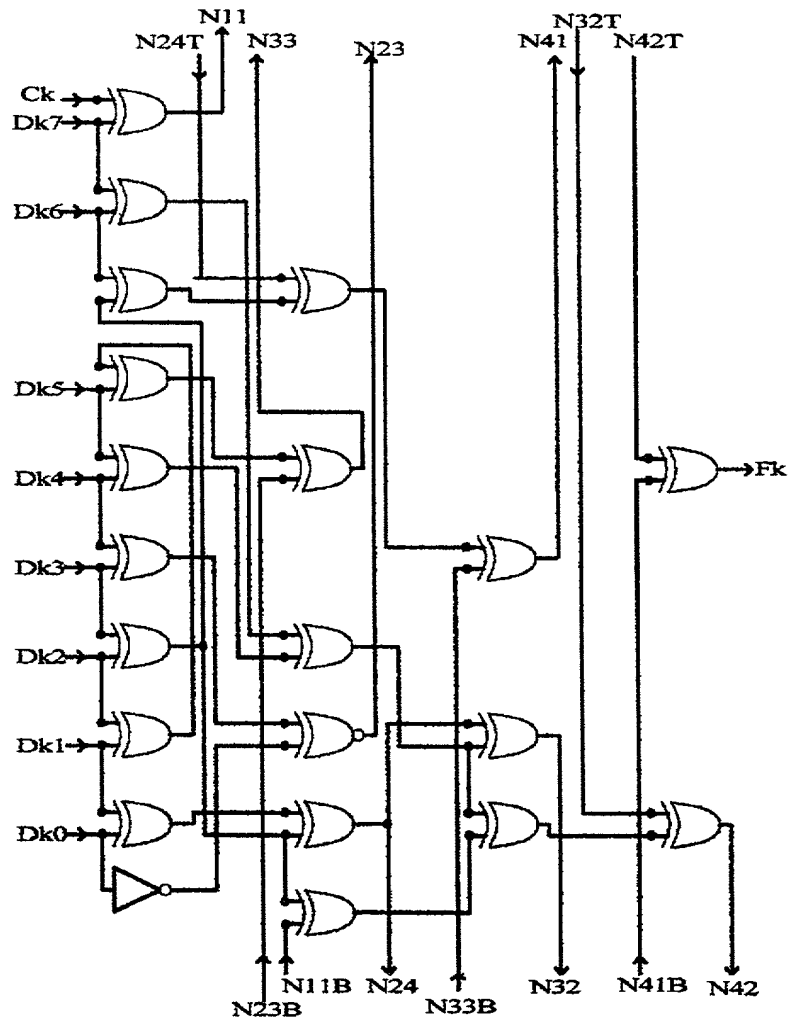


FIG. 2(c)

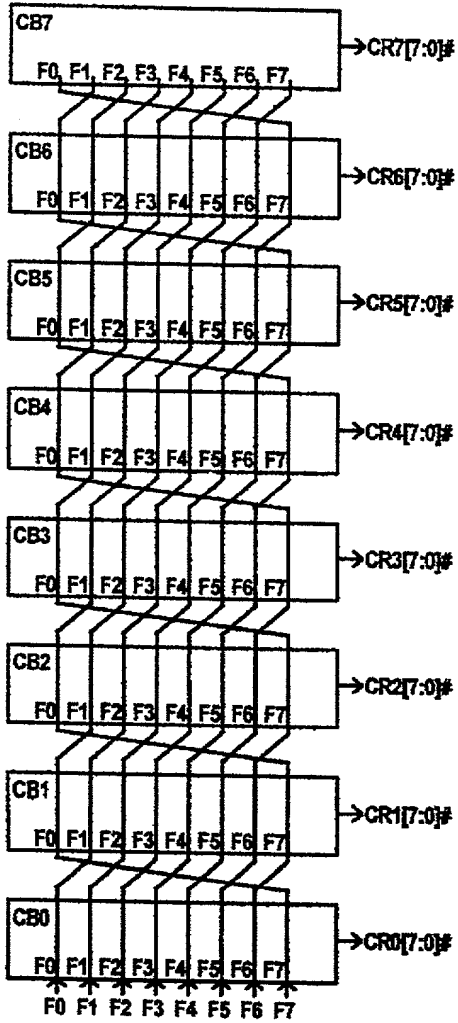


FIG. 2(d)

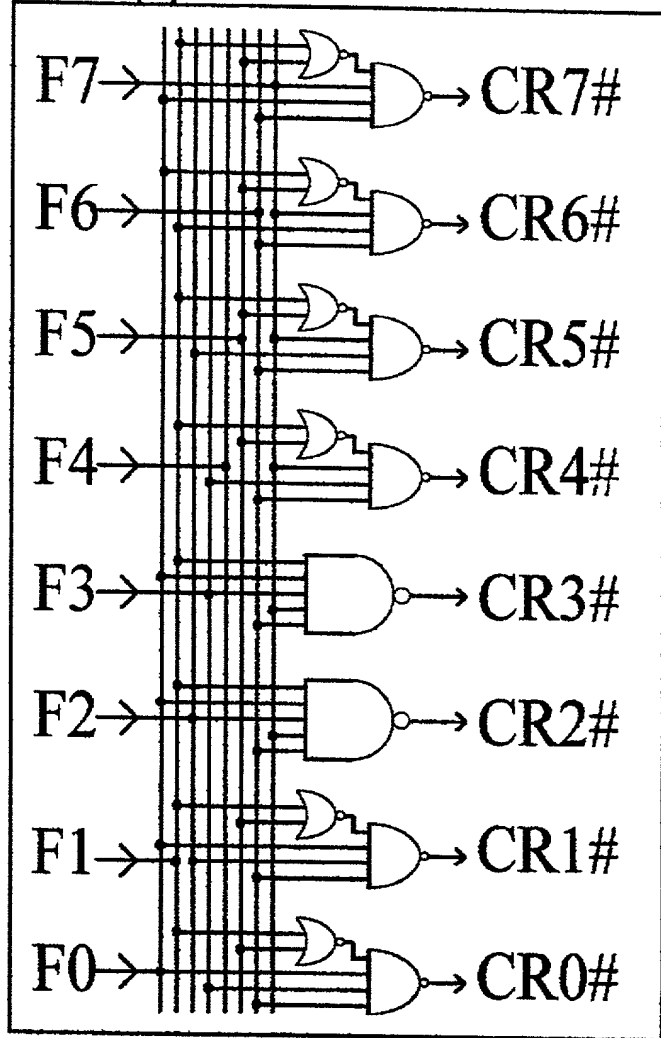


FIG. 3(a)

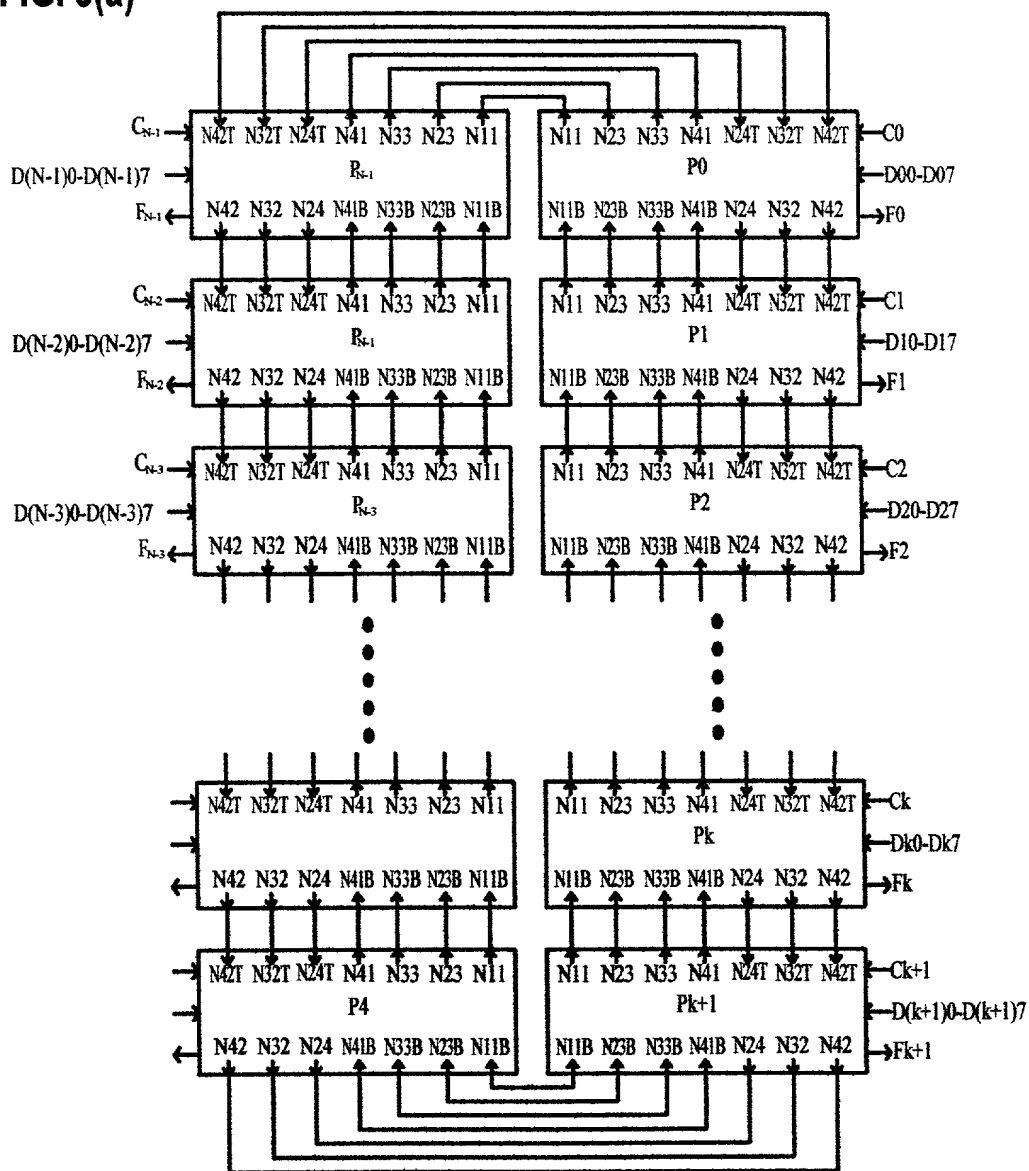


FIG. 4

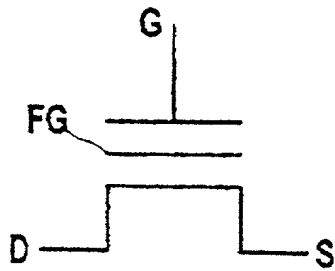


FIG. 5

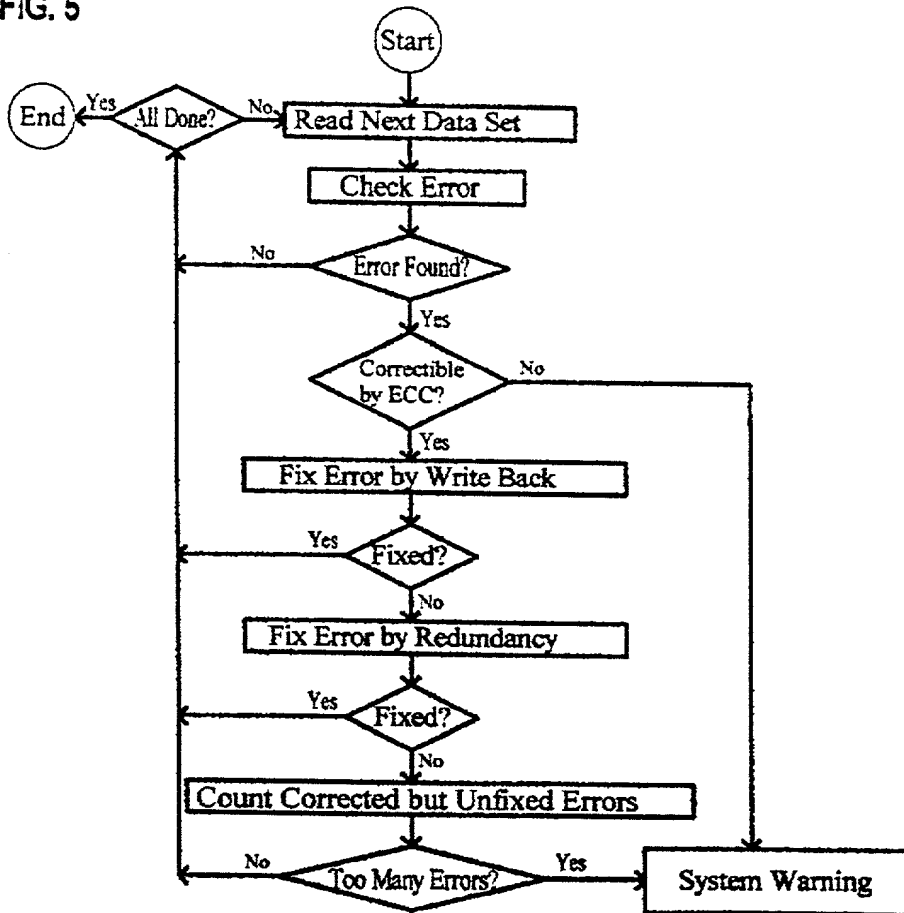


FIG. 6(a)

	D1D0	
Q3	1	1
Q2	1	0
Q1	0	1
	0	0

FIG. 6(b)

	D1D0	
Q3	1	0
Q2	1	1
Q1	0	1
	0	0

FIG. 6(c)

	D2D1D0		
Q7	1	0	0
Q6	1	0	1
Q5	1	1	1
Q4	1	1	0
Q3	0	1	0
Q2	0	1	1
Q1	0	0	1
	0	0	0

FIG. 6(d)

	D3D2D1D0			
Q15	1	0	0	0
Q14	1	0	0	1
Q13	1	0	1	1
Q12	1	0	1	0
Q11	1	1	1	0
Q10	1	1	1	1
Q9	1	1	0	1
Q8	1	1	0	0
Q7	0	1	0	0
Q6	0	1	0	1
Q5	0	1	1	1
Q4	0	1	1	0
Q3	0	0	1	0
Q2	0	0	1	1
Q1	0	0	0	1
	0	0	0	0

FIG. 6(e)

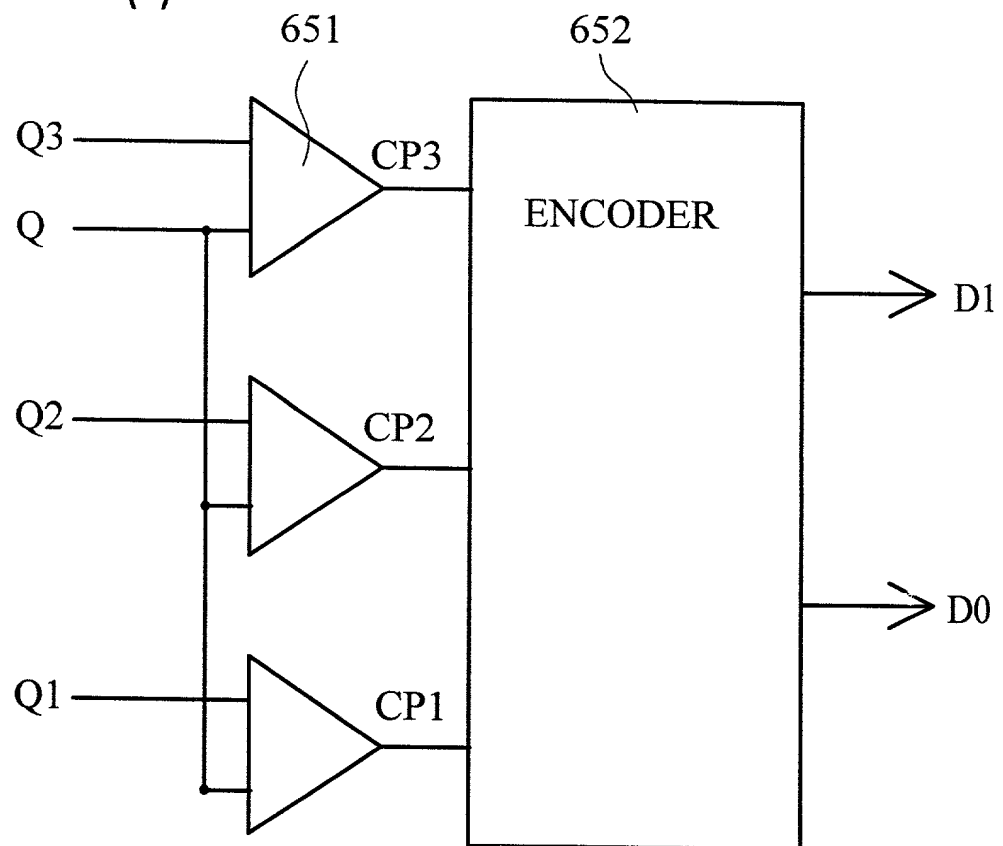




FIG. 7(a) Prior Art CAM

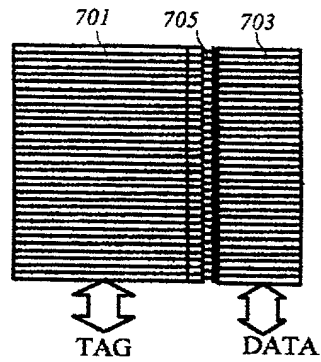


FIG. 7(b) Prior art 6T RAM cell

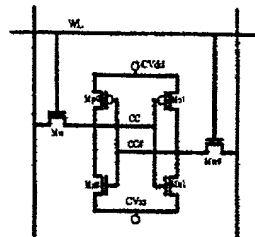


FIG. 7(c) Prior art 10T CAM cell

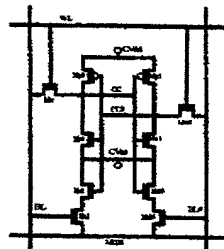


FIG. 8(a)

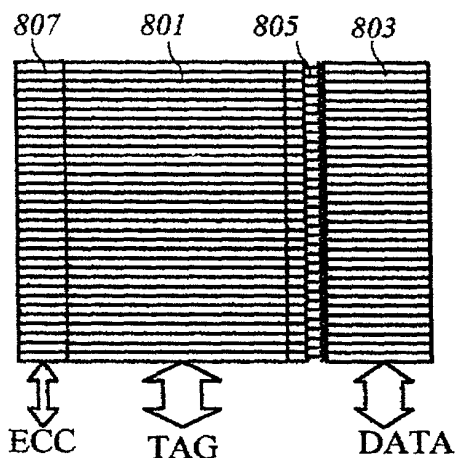


FIG. 8(b)

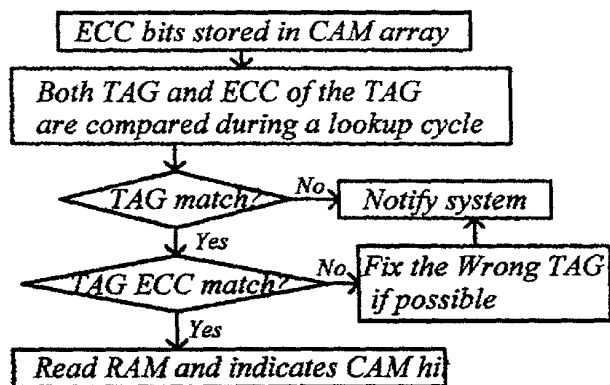


FIG. 8(c)

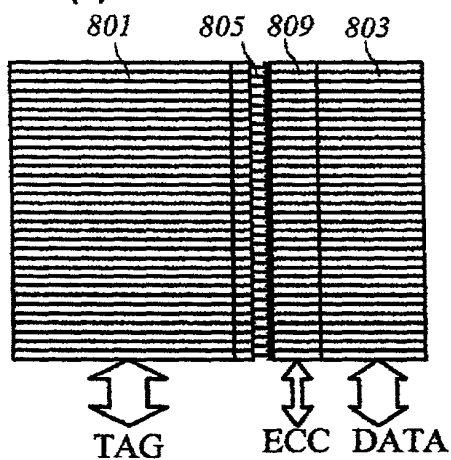


FIG. 8(d)

